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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Kouji Yamamoto

Serial No.: Not Yet Assigned

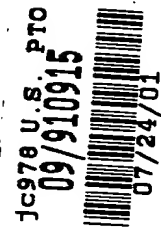
Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: SEMICONDUCTOR MEMORY DEVICE HAVING ECC TYPE ERROR
RECOVERY CIRCUIT

Assistant Commissioner of Patents
Washington, D.C. 20231



INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. In compliance with the concise explanation requirement under 37 CFR §1.98(a)(3), English-language Abstracts are attached to the references. It is noted that Japanese Patent Publication No. 5,134,616.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

Sean M. McGinn

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(Use several sheets if necessary)

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Not Yet Assigned

Kouji Yamamoto

Concurrently Herewith

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[illegible]

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	2539950	7/8/96	Japan			ABS	
	6-338199	12/6/94	Japan			ABS	
	8-222639	8/30/96	Japan			ABS	
	9-312343	12/2/97	Japan			ABS	

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.